

DESIGN AND DEVELOPMENT OF TEST JIG FOR SMART AND PREPAID ENERGY METER

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Abstract—In the rapidly evolving landscape of smart energy meters, ensuring the quality and reliability of printed circuit boards (PCBs) is important. This paper presents the design and development of a specialized test jig customized for the detailed testing of PCBs utilized in smart energy meters. The proposed test jig integrates advanced testing methodologies to assess the functionality, performance, and durability of PCB components under various operating conditions. Leveraging modular design principles, the test jig offers scalability and adaptability to accommodate diverse PCB designs and configurations. Furthermore, the test jig incorporates automated testing routines and comprehensive diagnostic features to streamline the testing process and enhance efficiency. Through rigorous validation and testing, the proposed test jig demonstrates its efficacy in facilitating accurate and efficient testing of smart energy meter PCBs, thereby contributing to the enhancement of product quality and reliability in the smart energy meter manufacturing industry.

I. INTRODUCTION

The rapid evolution of smart energy meter technology has significantly transformed the landscape of energy management and consumption monitoring. Smart energy meters play a crucial role in enabling accurate data collection, remote monitoring, and efficient energy utilization in residential, commercial, and industrial settings. However, ensuring the reliability and functionality of these devices necessitates rigorous testing procedures, particularly of their printed circuit boards (PCBs).

The design and development of a test jig specialized for testing the PCBs of smart energy meters are critical to guaranteeing the quality and performance of these essential components. This report outlines the process of conceiving, designing, and implementing such a test jig, highlighting the key considerations, challenges, methodologies, and outcomes involved.

Through this endeavor, the aim is to address the growing demand for efficient, fast, and reliable testing solutions in the smart energy meter industry. By providing insights into the design and development of a specialized test jig, this report helps to contribute in the advancement of quality assurance practices in the manufacturing and deployment of smart energy meters, ultimately fostering enhanced reliability, functionality, and sustainability in energy recording and management systems.

II. LITERATURE REVIEW

1. Test Jig Design Principles

Study by Zhang et al. [4] emphasizes the importance of modularity, scalability, and flexibility in test jig design for PCB

testing. A modular architecture is proposed that allows easy adaptation to different PCB layouts and testing requirements. Wang et al. [6] highlights the significance of incorporating automated test equipment (ATE) for higher throughput and accuracy in PCB testing. Their study focuses on the integration of ATE with test jigs for streamlined testing processes.

2. PCB Testing Techniques

Review by Smith et al. [1] discusses various PCB testing techniques, including boundary scan testing, in-circuit testing (ICT), and functional testing. Each technique offers unique advantages and challenges, influencing the design of test jigs. Research by Li et al. [4] introduces a novel approach combining machine learning algorithms with ICT for fault detection and diagnosis in PCBs. This hybrid method shows promising results in enhancing test accuracy and fault identification.

3. Smart Energy Meter PCB Testing Challenges

Chen et al. [6] identifies specific challenges in testing smart energy meter PCBs, such as the complexity of integrated circuits, high-frequency signal testing, and power consumption measurement. Addressing these challenges is essential in designing an effective test jig.

Case study by Kumar et al. [5] discusses real-world experiences in testing smart energy meter PCBs, highlighting common failure modes, testing bottlenecks, and solutions adopted to improve testing efficiency and reliability.

4. Integration of IoT and Industry 4.0 Technologies

Study by Yang et al. [7] explores the integration of Internet of Things (IoT) and Industry 4.0 technologies in PCB testing environments. They propose a cyber-physical system approach for real-time monitoring, remote control, and predictive maintenance of test jigs.

Research by Gupta et al. [9] investigates the potential of artificial intelligence (AI) and machine learning (ML) algorithms in optimizing test jig performance and predictive maintenance scheduling. Their findings suggest significant improvements in test efficiency and equipment lifespan.

Over 15+ papers were thoroughly studied in which various test jigs principles and methodologies are presented. But these technologies have some lacunas which are addressed through this project work.

III. METHODOLOGY

Government of India introduced a policy to replace all conventional meters with smart and pre-paid energy meter [12]. Thus, as per BIS, IS 16444 was redefined and various

entrepreneurs started research in this field and came up with several innovations in this field. One such firm, Shete Advanced Technology which is the only Indian company to have their software and hardware designed (made in India) itself started the manufacturing of smart and prepaid energy meters as per guidelines provided in IS 16444.

Smart meter being placed in consumer premises are highly sensitive. Smart meters need to be manufactured and calibrated to the required accuracy and tested for all the critical electrical parameters during the production stage itself for checking the operational performance.

With the reference of IS 16444 several check points on PCB were identified which are as shown in below Fig 1.

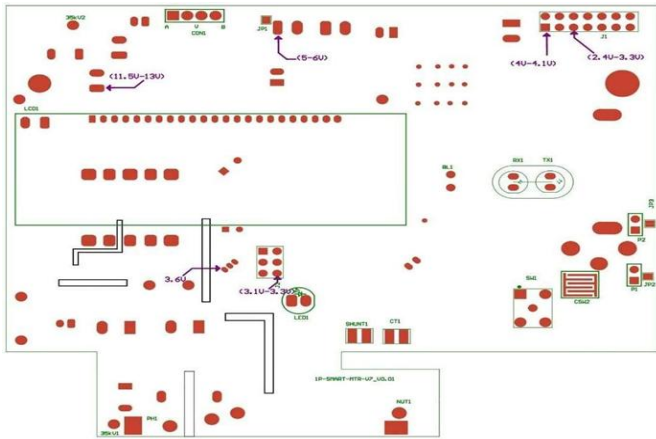


Fig 1: Check points on PCB

For checking those test points, test jig is crucial. Which is used to enhance functionality of PCB. Operation of jig is based on check points listed below:

- Voltage checking for power supply formation confirmation
- Battery current check for meter circuit
- Pulse checking on Phase & Neutral
- RTC set in PCB and erasing full memory

Thus, for the above purpose, basic layout of test jig (as shown in Fig 2) was designed replicating the operation and functioning of the jig.

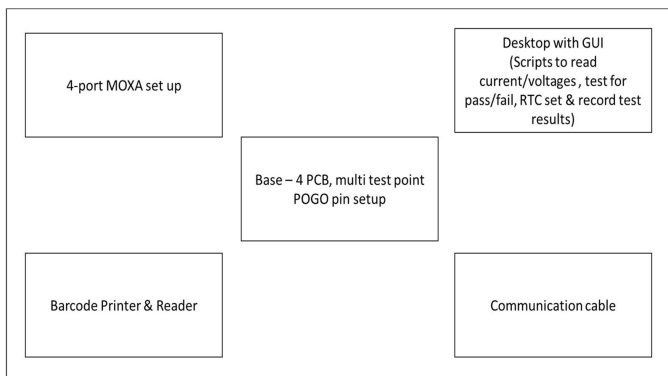


Fig 2: Basic layout of test jig

Hardware Description: -

For the design and development of test jig various components are used, which are listed below: -

1. Raspberry pie: -
Raspberry Pi was used as the brain of the test jig, controlling the entire testing process.

Through GPIO (General Purpose Input/Output) pins, it was interfaced with various components of the test jig such as PCB, sensors, etc.

Raspberry Pi serves as the central control unit in a test jig for smart energy meters, providing automation, communication with the device under test, data logging, analysis, and a user interface, all in a flexible and customizable package.



2. Step down transformer: -
Step down transformer was used to step down the voltage and current from 230V to 15 V.



3. Bridge rectifier: -
Bridge rectifier was used to convert the AC input power into a DC power.

Circuit Diagram: -

Various components were used in test jig such as raspberry pie, step down transformer, bridge rectifier, display, etc. Connection of all those components is shown in fig 3.

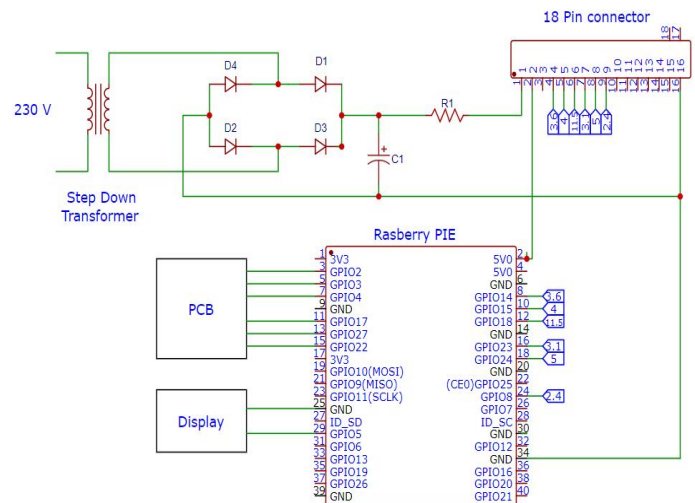


Fig 3: Circuit diagram of test jig

Technical Specifications: -

Test jig specifications and components were identified to fulfill the as per requirements of test to be performed and the operation of those was as per specifications given in the below table:

Sr. No.	Equipment	Specification
1	Step down transformer	230 V AC/3-15V DC
2	Bridge Rectifier	
3	Filter	
4	Raspberry pi	Broadcom BCM2837B0, contex-A53(ARMv8) 64-bit Soc,1GB RAM, GPIO pins for interfacing
5	USB cables	Data transfer rate: - up to 480 mbps
6	Pogo pins	Dia.: -1.36 mm
7	14 Pin Connector	Pitch: - 2-2.36 mm

Actual model: -

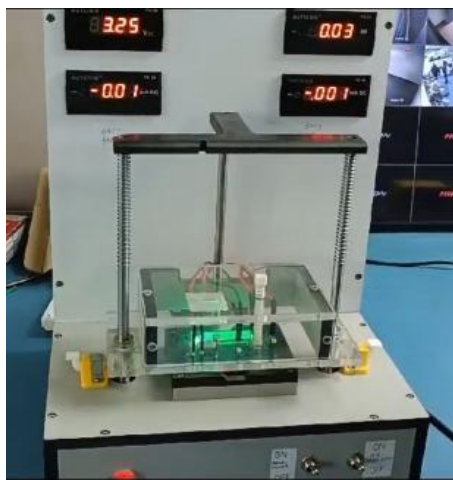


Fig 4: Actual model of test jig

IV. RESULTS

After placing the PCB on test jig, the anomaly of the PCB is checked. Applying voltage to various check points connected through Raspberry Pie and by using python programming, the functionality of the components is verified. So, there can be two conditions as mentioned below in Fig 5, 6 and 7: -

1. When all components are working well (OK Condition)

```
import matplotlib.pyplot as plt
x=[11.5,3.6,3.1,5,4,2.4]
y=[11.5,3.6,3.1,5,4,2.4]
plt.plot(x,y)
```

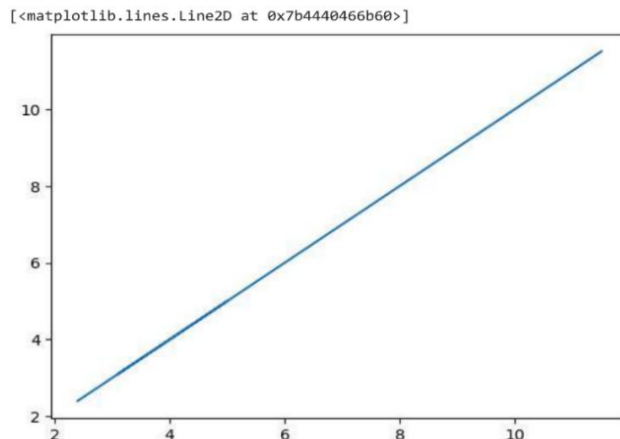


Fig 5: Case 1

2. When one or two components are malfunctioning (NOT OK Condition)

```
import matplotlib.pyplot as plt
x=[11.5,3.6,3.1,5,4,2.4]
y=[10,3.6,3.1,5,4,2.4]
plt.plot(x,y)
```

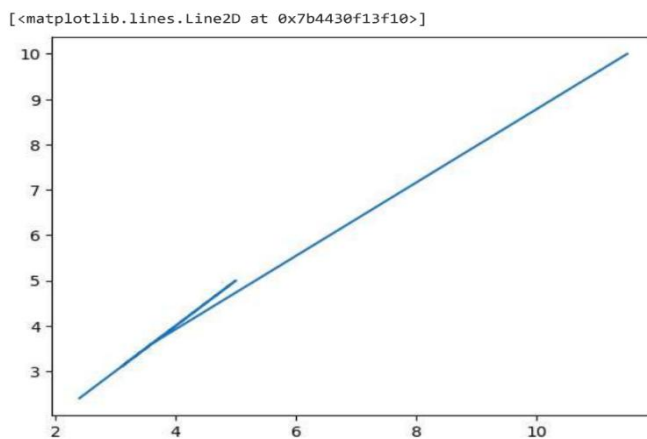


Fig 6: Case 2 a)

```
import matplotlib.pyplot as plt
x=[11.5,3.6,3.1,5,4,2.4]
y=[11.5,3.6,3.1,6,4,3.4]
plt.plot(x,y)
```

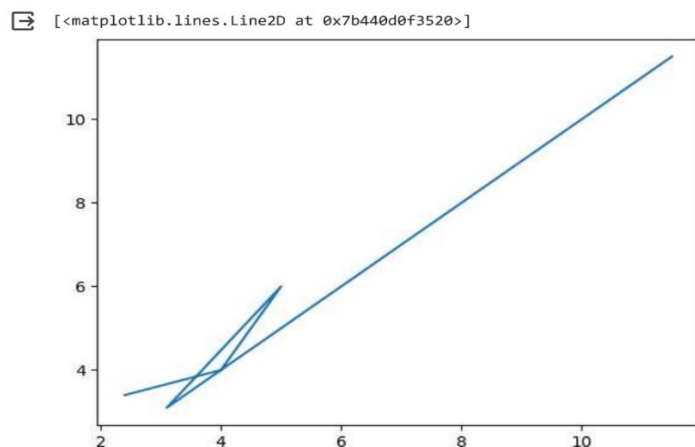


Fig 7: Case 2 b)

V. CONCLUSION

The methodology outlined above provides a systematic approach for designing and developing a test jig tailored to the specific requirements of testing PCBs in smart energy meters. By following this methodology, manufacturers can ensure the reliability, accuracy, and efficiency of the testing process, ultimately contributing to the quality and performance of smart energy meter products.

VI. ACKNOWLEDGEMENT

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